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First Inventor or Application Identifier Klebanov, et al
Title METHOD AND APPARATUS FOR
RECEIVING DIGITAL VIDEO SIGNALS
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APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents.	ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
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1. ☒ **Fee Transmittal Form**
(Submit an original, and a duplicate for fee processing)

2. ☒ **Specification** Total Pages 21
(preferred arrangement set forth below)
- Descriptive title of the invention
- Cross References to Related Applications
- Statement Regarding Fed sponsored R & D
- Reference to Microfiche Appendix
- Background of the Invention
- Brief Summary of the Invention
- Brief Description of the Drawings (if filed)
- Detailed Description
- Claim(s)
- Abstract of the Disclosure

3. ☒ **Drawings (35 USC 113) Total Sheets 8**

4. **Oath or Declaration** Total Pages 3

- a. ☒ Newly executed (original or copy);
b. ☐ Copy from a prior application
(37 CFR 1.63(d))

(for continuation/divisional with Box 17 completed)
(Note Box 5 below)

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Signed statement attached deleting
inventor(s) named in the prior application,
see 37 CFR 1.63(d)(2) and 1.33(b).

6. ☐ **Nucleotide and/or Amino Acid Sequence**
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- a. ☐ Computer Readable Copy
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ACCOMPANYING APPLICATION PARTS

7. ☒ **Assignment Papers** (cover sheet & document(s))

8. ☐ 37 CFR 3.73(b) Statement ☐ Power of
(when there is an assignee) Attorney

9. ☐ English Translation Document (if applicable)

10. ☐ Information Disclosure ☐ Copies of
Statement (IDS)/PTO-1449 IDS Citations

11. ☐ Preliminary Amendment

12. ☒ **Return Receipt Postcard (MPEP 603)**
(Should be specifically itemized)

13. ☐ Small Entity ☐ Statement filed in Prior
Statement(s) Application, Status still
proper and desired.

14. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)

15. ☐ Other

5. ☐ Microfiche Computer Program (Appendix)

16. If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No:

Prior Application Information: Examiner


Group / Art Unit:

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ilya Klebanov, et al

Examiner:

Serial No:

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Docket No: 0100.9900680

Title: METHOD AND APPARATUS FOR RECEIVING DIGITAL VIDEO SIGNALS

To the Honorable Commissioner
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METHOD AND APPARATUS FOR RECEIVING DIGITAL

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VIDEO SIGNALSField of the Invention

The present invention generally relates to a video capture system and its methods, and more particularly relates to capturing compressed and uncompressed digital video data.

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Background of the Invention

Digital Video Broadcast (DVB) transmission standards have been developed in order to support the emergence of digital television. As illustrated in prior art Figure 1, the DVB signal is sent via an analog carrier and received by a tuner. The tuner receives the transmitted analog signal and provides an analog representation of the signal to a demodulator portion. The demodulator portion converts the analog signal into its digital format. The digital signal provided by the demodulator is in a compressed MPEG 2 format. The compressed MPEG 2 format is referred to as a transport stream. The transport stream from the demodulator comprises a plurality of packets. Each of the transport stream packets comprises one synchronization byte, followed by one of 187 data bytes or 187 data bytes plus 16 extra bytes depending on the format. The resulting transmission stream packets have a total byte size of one of 188 bytes, or 204 bytes.

The data contained within each packet of the transmission stream can represent video data, audio data, system data, and other user data such as programming information. In other words, more than just video data is provided within the transmission stream. For example, user guides indicating channel selection information or stock market information could be included within a transmission stream packet. Because the transport stream data is contained in a compressed format, when video is being transmitted, there is not a one-to-one correspondence

between the actual bytes being transmitted in the transport stream and the pixels which the data represents. Because the data is compressed, one packet of transmission stream data can represent varying numbers of pixels in a video system.

A transport demultiplexor receives the digital transport stream as illustrated in prior art Figure 1. The transport demultiplexor is capable of routing the individual components represented within the transport stream to the respective clients. In other words, audio data could be provided to an audio client while video data would be provided to a video client. For example, the transport demultiplexor can route data by providing address information to the PCI bridge of Figure 1.

Also illustrated in the prior art of Figure 1 is a Peripheral Component Interconnect (PCI) Bus supporting conventional computer-type peripherals. Connected to the PCI Bus of Figure 1 are a Memory, a central Processor Unit (CPU), and a Video Adapter. In order to support the reception and subsequent transmission of the transport stream to one of the system components, such as to the Memory or the Video Adapter, a Converter/PCI Bridge has been used. The converter portion is necessary in order to change the transport stream into data packets capable of being transmitted across a system bus, such as through the PCI bridge. Such a conversion would require converting the 188-byte packet of the transport stream into 32-bit or 64-bit wide words of information and transporting the words to Memory or the Video Adapter across the PCI Bus.

In order to provide data to the video adapter of the prior art, it is necessary for separate printed circuit boards to be used to implement the transport stream converter and video adapter. Separate boards allows a PCI interface to the video memory associated with the video adapter. Separate board increase overall system costs. In addition to increasing overall system costs, the data stream data requires approximately 5 Mbytes of PCI bandwidth, thereby limiting the bandwidth available to other system resources. In addition, when analog video is received and digitized (not illustrated), by the prior art system the PCI band data bandwidth is approximately 25 Mbytes.

Another proposed method for receiving DVB data was to use the side port of a video graphics adapter in order to receive the transport stream information. However, the video side

port is designed to only receive uncompressed digital video instead of compressed MPEG transport stream. A format conversion chip is needed between the DVB demodulator output and video side port to convert a transport stream into a compatible ITU-656 ancillary stream (digital video or data format) like format. This will add cost to system implementation. Another problem is that the data in the transport stream is not fully compliant with ITU-656 data. (values such as 00 and FF are not allowed in an ITU-656 stream but allowed in a transport stream. So this implementation cannot work if the video side port is strictly designed for ITU-656 data streams.

Therefore, a method and apparatus, which overcome the problems of the prior art, would be advantageous.

Brief Description of the Drawings

Figure 1 illustrates, in block diagram form, a prior art system for receiving a transport stream;

Figure 2 illustrates, in block diagram form, a system in accordance with the present invention;

Figure 3 illustrates, in block diagram form, a detailed view of a portion of Figure 2;

Figures 4 and 5 illustrate, in block diagram form, a detailed view of a portion of Figure 3;

Figure 6 illustrates an active video area in a video frame.

Figures 7 and 8 illustrate, in timing diagram form, timing signals associated with one type of digital video;

Figure 9 illustrates, in timing diagram form, timing signal associated with a transport stream;

Figure 10 illustrates, in block form, a memory associated with Figure 5; and

Figure 11 illustrates, in flow diagram form, a method in accordance with the present invention.

Detailed Description of the Drawings

5 A method and apparatus for receiving one of a compressed video stream and an uncompressed video stream is disclosed. The uncompressed video stream may be Zoom Video data or a digitized analog video signal. The compressed video stream may be an MPEG transport stream data from a High Definition television (HDTV) broadcast. A Video Graphics Adapter is configured to properly receive one of the two types of video data. The received data
10 and control signals are monitored to provide a second set of control signals that are used by a packer, a window control, and an address generator. The packer packs data into a format which is compatible with the frame buffer memory. The window controller controls the amount of data written into frame buffer memory. The address generator generates proper frame buffer addresses for the data. The data is stored within a pre-defined area of graphics memory such as a
15 frame buffer. The data can be transferred to system memory when a buffer is full.

Figure 2 illustrates a system in accordance with the present invention. A digital video broadcast (DVB) signal is received by the tuner 210. The tuner 210 provides a representation of the received analog signal to the demodulator 220. The demodulator 220 demodulates the signal to provide a digital TRANSPORT STREAM to one or both of the Transport Demultiplexor 240,
20 and the Video Adapter 230. In accordance with the present invention, the Video Adapter 230 receives the TRANSPORT STREAM and buffers it into a video memory, or frame buffer. Upon filling the frame buffer, the transport stream data is either further utilized by the Video Adapter 230, or at least partially provided to system components, such as the central processing unit (CPU) 250, or the memory 260. A second data path receives an analog signal, such as would
25 normally be associated with television broadcasts, at tuner 211. The signal from tuner 211 is provided to a NTSC/PAL/SECAM demodulator 221 to provide a digital representation of the received analog signal. Not that the tuner 211 and demodulator 221 may be the same, or different, from the tuner 210 and demodulator 211.

Figure 3 illustrates the Video Adapter 230 in greater detail. In normal operation, the Video Adapter 230 processes video and/or graphics information and provides a signal labeled VIDEO OUT. The VIDEO OUT signal is used to drive an image onto an external display device (not shown). In accordance with the present invention, the Video Adapter 230 includes a
5 Capture Block 310 to receive a data stream, a graphics engine 320, a graphics memory 231, and a PCI interface 340.

In operation, the a digital data stream, such as the TRANSPORT STREAM from the demodulator 220 of Figure 2, is received at the capture block 310. Where the digital data stream is a TRANSPORT STREAM, the TRANSPORT STREAM comprises both data, and control
10 information. Generally, the TRANSPORT STREAM includes a plurality of packets. Each packet of transport stream information includes a synchronization byte followed by a predetermined number of data bytes. The data bytes can include routing information stored as header information, or as raw data as identified by the header information. When the data is transmitted as header information, it is in an uncompressed form that indicates the type of data
15 that is to follow the header. A single header can be included in one or more packets.

The Capture Block 310 receives the TRANSPORT STREAM information and provides the necessary data and control in order to store the compressed transport stream data within the Graphics Memory 330. The memory 330 is accessed over the bus 350. In a specific embodiment, the memory 330 is a frame buffer used by the Graphics Engine 320. The Memory
20 330 is connected to the Capture Block 310 through the bus 350 which accommodates the necessary data, address, and control lines for accessing memory 330. The graphics memory 330 is also connected to the graphics engine 320 and the PCI Interface 340 through the bus 350.

In accordance with the present invention, the graphics Memory 330 can operate as a frame buffer for the Graphics Engine 320, or as a buffer to store the TRANSPORT STREAM data. The PCI Interface 340 provides an interface between the internal bus 350 to an external
25 PCI bus. Generally, the PCI Bus will be a system bus. It should be noted that while a PCI interface 340 has been shown, the present invention anticipates the use of other type bus interfaces. Therefore, the PCI interface 340 may be any bus type whether an industry standard or a proprietary interface.

Figure 4 illustrates the capture block 310 in greater detail. Specifically, the capture block 310 is shown to receive a number of different types of video data. As indicated, 8-bit DVS (Digital Video Stream) video and ZOOM VIDEO is received. Generally, the 8-bit DVS and ZOOM VIDEO data are ITU-601 or ITU-656 related digital video streams. These streams are not compressed video streams. However, the HDTV transport stream received by the capture block 310 of Figure 4, as previously mentioned, is a compressed video stream, such as MPEG 2. While the DVS, ZOOM and TRANSPORT STREAM are illustrated in Figure 4 separately, in operation, the signals will share inputs that can be multiplexed, or de-multiplexed as needed. For example, the data bits from each format can be received by a common set of pins

A signal labeled VIDEO SELECT is used to indicate to the Capture Block 310, which type of video is to be received. By allowing for one of a plurality of types of digital video data to be received, greater flexibility is realized within the video graphics adapter (VGA). This reuse of common circuitry allows for an efficient implementation of the present invention. Yet another advantage of the system as illustrated in Figures 3 is that it reduces system costs over the prior art in that the PCI interface 340 already residing within the VGA is used to store transport buffered stream data.

Figure 5 illustrates the capture block 310 in greater detail. Figure 5 will be discussed with reference to Figures 6-10, which illustrate specific embodiments of the present invention including various timing diagrams.

In Figure 5, the TRANSPORT STREAM, and any other type of digital video, such as ZOOM VIDEO, is received by the Video-In Controller 510 of Figure 5. The TRANSPORT STREAM includes the signals 501, which include a multi-bit data signal (DATA), a synchronization signal (SYNCH), a data valid signal (DVALID), and a clock signal (TCK). The ZOOM VIDEO data is illustrated as a bus. However, it should be noted, that the signals 501 can be multiplexed and/or de-multiplexed (not shown) to receive either ZOOM VIDEO or TRANSPORT STREAM data.

The Video-In Controller 510 acts as a stream interface control to convert the received signal, whether ZOOM VIDEO or a TRANSPORT STREAM, into a Start of Field (SOF) signal, a Start of Active (SOA) signal, an End of Active (EOA) signal, a Data Active (DACTIVE)

signal, and a Video Data (VDATA) signal. For ZOOM video, these signals are represented in Figures 6 and 7.

Figure 6 represents a frame of video 610. In a specific embodiment, the video is representative of ZOOM VIDEO. The frame of video 610 has a Vertical Blanking Interval (VBI) which resides in the first few lines of video. The VBI information is not displayed, but can be used to provide data for other operations of functions. Following the VBI portion, VIDEO is provided. Within the VIDEO portion, an Active Video 620 is illustrated which represents a portion of the VIDEO to actually be displayed. At the beginning of each line of the frame 610, a Start of Active (SOA) pulse is provided. At the end of each line of the frame 610, an End of Active video (EOA) signal is provided. At the beginning the first line of the frame 610, the start of a new frame is indicated by a Start of Frame (SOF) indicator. Note that the SOA and EOA refers to the active video relative to the ZOOM VIDEO standard which is the entire VIDEO portion is considered active video relative to the transmitted data. The Active Video 620 is the Active Video relative to the user.

The Active Video 620 of Figure 6 indicates the portion of the received VIDEO to be displayed. One example of the Active Video 620 varying from the received VIDEO is when the received video is High Definition Television (HDTV), which has a different aspect ratio than standard television, is to be displayed on a standard television. In this mode of operation, it is desirable to select only a portion of the HDTV frame for display on a standard television screen. In order to specify the desired Active Video 620, it is necessary to specify a Y OFFSET indicated where the first line of Active Video 620 begins, a X OFFSET indicating which pixel is the first pixel of Active Video 620, a WIDTH indicating the number of pixels in a containing Active Video 620, and a HEIGHT indicating the number of lines containing the active Video. Note that the X OFFSET, and Y OFFSET are illustrated to be relative to the first line and pixel of the frame 610. However, in other embodiments, other reference locations can be indicated.

Figure 7 illustrates the signals associated with ZOOM VIDEO. The ZOOM VIDEO signals received by the Video-In Controller 510 are substantially similar to the signals provided by the Video-In Controller 510, except that no DACTIVE signal is received. Therefore, since

there is no equivalent signal to DACTIVE in Zoom video, the DACTIVE signal from video-in controller 510 specified is asserted when the Video-In Controller 510 receives data.

In operation, the Window Control 520 receives the SOF, SOA, and EOA signals from the Video-In Controller 510. In addition, the Window Controller 510 receives, and/or has access to values indicating the X OFFSET, Y OFFSET, WIDTH, AND HEIGHT values associated with Figure 6. These values can be provide in any number of manners, including inputs to the Window Controller 520, or by accessing register locations. From the received values and signals, the Window Control 520 generates a second set of control signals: Window control Start of Field (WSOF); Window control End of Field (WEOF); Window control End of Line (WEOL); Vertical Active (VACTIVE); and Horizontal Active (HACTIVE). These signals are further described with reference to the table below, and the relationship of the signal WSOF, WEOF, AND WEOL are illustrated in Figure 8.

VIDEO-IN OPERATION FOR ZOOM VIDEO

SIGNAL	DESCRIPTION
WSOF	Pulse active when at first pixel of Active Video.
WEOF	Pulse active at last pixel of Active Video.
WEOL	Pulse active at end of each line of Active Video.
HACTIVE	Asserted when pixel count within Width.
VACTIVE	Asserted when line count is with in HEIGHT.

The signal WSOF (Window control Start of Field) provides an asserted pulse when the first pixel of the Active Video 620 is encountered. The first pixel location can be determined by comparing the X OFFSET and Y OFFSET values to the current pixel and line numbers, which are maintained by the system. For example, in one implementation, the Y OFFSET represents the first line of Active Video 620, while the X OFFSET represents the first pixel of Active Video 620 within a line. This is represented in Figure 8, where the signal WSOF is active at the same

there is no equivalent signal to DACTIVE in Zoom video, the DACTIVE signal from video-in controller 510 specified is asserted when the Video-In Controller 510 receives data.

In operation, the Window Control 520 receives the SOF, SOA, and EOA signals from the Video-In Controller 510. In addition, the Window Controller 510 receives, and/or has access to values indicating the X OFFSET, Y OFFSET, WIDTH, AND HEIGHT values associated with Figure 6. These values can be provide in any number of manners, including inputs to the Window Controller 520, or by accessing register locations. From the received values and signals, the Window Control 520 generates a second set of control signals: Window control Start of Field (WSOF); Window control End of Field (WEOF); Window control End of Line (WEOL); Vertical Active (VACTIVE); and Horizontal Active (HACTIVE). These signals are further described with reference to the table below, and the relationship of the signal WSOF, WEOF, AND WEOL are illustrated in Figure 8.

VIDEO-IN OPERATION FOR ZOOM VIDEO

SIGNAL	DESCRIPTION
WSOF	Pulse active when at first pixel of Active Video.
WEOF	Pulse active at last pixel of Active Video.
WEOL	Pulse active at end of each line of Active Video.
HACTIVE	Asserted when pixel count within Width.
VACTIVE	Asserted when line count is with in HEIGHT.

The signal WSOF (Window control Start of Field) provides an asserted pulse when the first pixel of the Active Video 620 is encountered. The first pixel location can be determined by comparing the X OFFSET and Y OFFSET values to the current pixel and line numbers, which are maintained by the system. For example, in one implementation, the Y OFFSET represents the first line of Active Video 620, while the X OFFSET represents the first pixel of Active Video 620 within a line. This is represented in Figure 8, where the signal WSOF is active at the same

time as the clock pulse labeled L. The clock pulse L represents the time at which the first pixel of the Active Video 640 is available.

The signal WEOF provides an asserted pulse when the last pixel of the Active Video 620 is encountered. The last pixel location can be determined by comparing the sum of the WIDTH and X OFFSET values to the current pixel number, and comparing the sum of the HEIGHT and the Y OFFSET values to the line number. For example, in one implementation, the X OFFSET plus the WIDTH less one indicates the last bit of Active video 620 associated with a line. Likewise, Y OFFSET plus the HEIGHT less one indicates the last line of Active Video 620. Therefore, by monitoring these values, the WEOF signal can provide an asserted pulse when both the last line and pixel of an Active Video 620 region is encountered. This is illustrated in Figure 8, where the signal WEOF is active at a time in conjunction with the clock pulse labeled N. The clock pulse N represents the time at which the last pixel of the Active Video 640 is available.

The signal WEOL provides an asserted pulse when the last pixel of a line of Active Video 620 is encountered. The last pixel of a line can be determined by comparing the sum of the WIDTH and X OFFSET values to the current pixel number. For example, in one implementation, the X OFFSET plus the WIDTH minus one represents the last bit of a line of Active Video 620. This number can be compared to the current pixel number to determine whether the end of line has occurred. WEOL is represented in Figure 8, where the signal WEOL is active at a time in conjunction with the clock pulses labeled M and N. The clock pulses M and N represents the time at which the last pixel of a line of data is available.

The signal HACTIVE is asserted when the current pixel location is within the WIDTH region. Note that a pixel does not have to be in the Active Video 620 in order for HACTIVE to be asserted. This allows for an embodiment whereby only the pixel number is monitored and compared to the values associated with the WIDTH of the Active Video 620. Likewise, the VACTIVE signal is asserted when the current line number is within the HEIGHT region indicated in Figure 6.

It will be understood by one skilled in the art, that the exact time at which the Window Controller 510 provides a given control signal can vary depending upon specific embodiments.

For example, the WEOL signal can be asserted in conjunction with the last pixel, or after the last pixel.

Referring to Figure 5, the signals generated by the Window Controller 510 are used by the Packer 640 and the Address Generator 630 to provide data and address to the buffer. In one embodiment, the Packer 640 receives 8-bit bytes of data, and combines them to form larger data words labeled VIDEO DATA. For example, the VIDEO DATA signal can comprise 32, 64, or 128 bit words of video data. The width of VIDEO DATA can be fixed or programmable. The DACTIVE, HACTIVE, and VACTIVE signals qualify the VDATA received by the Packer 640.

For ZOOM VIDEO, DACTIVE is always asserted. Therefore, the Packer 640 uses the HACTIVE and VACTIVE data to qualify VDATA. For example, when both DACTIVE and VACTIVE are asserted, the data value received on VDATA is within the ACTIVE VIDEO 620, and will be included by the Packer 640 within the VIDEO DATA. If either of DACTIVE and VACTIVE are inactive, the data value received on VDATA is not within the ACTIVE VIDEO 620, and will be not included by the Packer 640 as part of the VIDEO DATA.

The described embodiment for receiving ZOOM VIDEO provides for an efficient means to receive only a desired portion of video data, however, the data received needs be readily associated with a specific line and a pixel in order to determine where the ACTIVE VIDEO resides. In contrast, DVB data is compressed, and visibility as to the line and pixel locations is not readily available without decompression of the data first occurring. In specific embodiments, it is desirable to perform such decompression by other parts of the system, or at least to store the compressed data in other parts of the system. Therefore, one embodiment of the present invention further allows compressed data, such as DVB transport stream data, to be buffered within the Graphics Memory 330 of Figure 3.

Figure 9 illustrates a timing diagram representing signals associated with the TRANSPORT STREAM of Figure 2. The TRANSPORT STREAM includes a signal labeled TCK, which provides one clock pulse to qualify each byte of data.. A synchronization (SYNCH)

signal indicates the beginning of a new line or packet of information. In addition, a data valid signal (DVALID) indicates when the packet data being transmitted is valid.

In response to the TRANSPORT STREAM, the Video-In Controller 510 drives the signals SOF, SOA, EOA, DACTIVE, AND VDATA. The manner in which these signals are generated is different for a TRANSPORT STREAM than for the ZOOM VIDEO previously discussed. The table below indicates the operation of the Window Controller 510 for TRANSPORT STREAM reception.

OPERATION FOR RECEPTION OF A TRANSPORT STREAM

VIDEO-IN		
SIGNAL		DESCRIPTION
-----		-----
SOF		Pulse active to indicate the first byte of a transport stream packet to be stored in buffer.
SOA		Pulse active to indicate the first byte of a transport stream packet.
EOA		Pulse active to indicate the last byte of a transport stream packet.
DACTIVE		Asserted to indicate invalid bytes as indicated by DVALID.
DATA		Data from TRANSPORT STREAM.

During reception of the TRANSPORT STREAM, the SOF signal provides an asserted pulse to indicate that the first byte to be stored in the Graphics memory 330 is present. In one embodiment, the Video-In Controller 510 will use a counter to keep track of the number of lines and bytes of compressed data provided to the Window Controller 510. When the number of lines sent to the Window Controller 510 exceeds the number of lines available in the Graphics Memory 330, the SOF is pulsed again to indicate a new first byte to be stored in the graphics memory. The SOF signal of Figure 9 illustrates two pulses. The first pulse corresponds to TCK 1, which is associated with the first data byte to be stored in the Graphics memory 330. The second SOF pulse occurs on the first TCK after line N has been transmitted, where N is the total

number of lines to be stored in the Graphics memory 330. Note that in Figure 9, the number of bytes in a line of video memory is 88. Coincidentally, this is the same number of bytes that are in a DATA STREAM packet. In other embodiments, the line size is different than the packet size, such that a line of video memory will not necessarily contain an integer number of packets.

5 Note that the Graphics Memory 330 can have one or more buffer locations. Where multiple buffer locations are present, it is possible to switch between buffers when one buffer is full, and continue storing data without delay, or with minimal delay, in a second buffer. If only one frame buffer is available, and it becomes full, it will be necessary to write at least some of the Graphics Memory 330 contents to system memory, or lose data by writing over the stored
10 values. By using a dual ported Graphics Memory 330, it would be possible to buffer data and transmit it to the system simultaneously. In another embodiment, a single ported memory can also do the job, by interleaving between read and write operations.

 The Video-In Controller 510 signal SOA indicates that the first byte of a TRANSPORT
STREAM packet is being transported. The SOA signal can be qualified by the TRANSPORT
15 STREAM's SYNCH signal. There are three such SOA pulsed indicated in Figure 9.

 The EOA signal indicates that the last byte of a TRANSPORT STREAM packet is being
transmitted, or has been transmitted. In one embodiment, the generation of the EOA signal is
determined by utilizing a counter, state machine, or ALU to determine when the predetermined
number of bytes have been transmitted. Because the signal can be corrupted, there may not be
20 the predetermined number of clock signals before the next SYNCH pulse occurs. Therefore, in
another embodiment, a clock independent of the TCK can be used to determine when to assert
the EOA signal. Such a clock can be phase locked to the SYNCH signal to maintain an accurate
indication of when EOA is to occur.

 The signal DACTIVE indicates when the data presented to the Packer 640 is valid. As
25 indicated in Figure 9, the signal DACTIVE will generally mirror the signal DVALID of the
TRANSPORT STREAM.

 The signal VDATA will generally be the TRANSPORT STREAM data bytes
(DATA(7:0)).

The Window Control 520 receives the SOF, SOA, and EOA signals from the Video-In Controller 510 representing the TRANSPORT STREAM. In addition, the Window Controller 510 receives, and/or has access to, values indicating the X OFFSET, Y OFFSET, WIDTH, AND HEIGHT values associated with Figure 6. For TRANSPORT STREAM reception, the X
 5 OFFSET AND Y OFFSET will generally be zero, the width will be number of bytes in the TRANSPORT STREAM, and the HEIGHT will indicate the number of lines to be stored in the Graphics Memory 330. From the received values and signals, the Window Control 510 generates the signals: Window control Start of Field (WSOF); Window control End of Field (WEOF); Window control End of Line (WEOL); Vertical Active (VACTIVE); and Horizontal
 10 Active (HACTIVE). These signals are further described with reference to the table below.

WINDOW CONTROL OPERATION FOR TRANSPORT STREAM DATA

	SIGNAL	DESCRIPTION
15	WSOF	Pulse qualified to SYNCH signal and counter
	WEOL	Pulse qualified to EOA.
	WEOF	Pulse qualified to SOF and EOA.
	HACTIVE	Assertion qualified to SOA and EOA when pixel count within Width.
20	VACTIVE	Asserted when between SOA pulse and EOA pulse.
	HACTIVE	Asserted when between SOF pulse and WEOF.

The signal WEOL provides an asserted pulse when the last byte of a TRANSPORT
 25 STREAM packet is encountered. Generally, WEOL signal will be based on the EOA signal.

The signal WSOF signal provides an asserted pulse to indicate the first byte to be stored within a buffer of the Graphics Memory 330 buffer. For a specific embodiment, where the Video-In Controller 510 monitors and maintains the number of lines being written, the WSOF

signal for a TRANSPORT STREAM will be analogous to the SOF signal generated by the Video-In Controller.

The signal WSOF signal provides an asserted pulse to indicate the last byte to be stored in the current buffer location of the Graphics Memory 330. The Window Controller 510 generates the WSOF signal by comparing the number of EOA signal received since SOF was active, and comparing this number to the provided HEIGHT value. By setting the Y OFFSET to zero, and the HEIGHT value to the number of lines in the Graphics Memory 330, it is possible for the Window Controller 510 to generate the correct WEOF pulse without additional hardware.

The signal HACTIVE is asserted when the current byte location is within the WIDTH region which is set to the number of bytes in a packet (188 bytes).

The signal VACTIVE is asserted when the current data being received is to be stored be stored in the current line of the buffer.

The Packer 640 provides a signal labeled ADDR GEN REQ, which indicates when a line of data is ready to be stored in the Graphics Memory 330. The Graphics Memory 330 address and control information, is provided to the Graphics Memory 330 by the Address Generator 630 when ADDR GEN REQ is active.

With TRANSPORT STREAM data, it is possible for invalid data to be received. In order to provide visibility to other system portions as to when the stored TRANSPORT STREAM data is valid, a VALID byte location can be included as part of the buffer. Figure 10 illustrates one such VALID byte. By storing a unique value in this byte location, subsequent systems can be notified of the invalid data. In another embodiment, specific values can be stored to indicate those times when invalid data is received. In yet another embodiment, the entire line could be flushed, thereby only permitting valid lines of data to be stored. For types of uncompressed video where the valid byte is not needed, normal video data can be stored at the VALID location.

An error signal can also be incorporated into the transport stream capture. When an error signal is received, a specific code can be generated and written with the data stream. Subsequent systems can be notified that an error occurred by monitoring the data and react accordingly.

In this manner, compressed and uncompress data can be stored in the frame buffer represented by Graphics Memory 330. Once an entire frame of data is stored in the Graphics Memory 330 the data can be written to system memory, or it can be decompressed by the Graphics Engine 320.

- 5 By having the Video-In Controller 510 generate the SOF, SOA, EOA, DACTIVE, and VDATA in the manner indicated, it is possible to use much of the same hardware to implement a storage apparatus for both compressed and uncompressed video.

Figure 11 illustrates a method in accordance with the present invention. Generally, the method of Figure 11 has been discussed with respect to the specific system herein. At step 1101,
10 a determination is made whether a first or second type of video data is to be received. Specifically, the first type of data represents TRANSPORT STREAM data as indicated in step 1102. The second type of data represents a digital video stream different from a TRANSPORT STREAM, such as the ZOOM VIDEO signal discussed herein, as indicated at step 1103

At steps 1102 and 1103 the system is configured for either a TRANSPORT STREAM or
15 a different digital video stream respectively. As discussed with respect to TRANSPORT STREAM and ZOOM Video herein, these configurations indicate how a video controller will generates its output signals.

At step 1104, a secondary set of control signals is generated from the received data stream. This corresponds to the Video-In Controller 510 generating signals SOF, SOA, EOA,
20 DACTIVE, AND VDATA, as discussed herein.

At step 1105, at least a portion of the data stream is stored. Step 1105 corresponds to the Window Controller 510 , Packer 640, and Address Generator 530 working together as a data storage controller to store words of data in a buffer associated with Graphics Memory 330. These words of data will contain at least a portion of the data received by the Video-In
25 Controller 510 in the manner discussed herein.

At step 1106, the information stored in the Graphics Memory 330 is written to a system bus via the PCI Interface 340 of figure 3.

The present invention has been described with reference to specific embodiment. One of ordinary skill in the art will recognize that other embodiment may exist. For example, the data storage described herein has been described with reference to using SOF/EOF/EOA/EOA video fields to store transport stream data. In a similar manner, fields such as Start of VBI and End of VBI could be used to indicate when to store the transport stream data. Such an implementation would require the Window Controller 520 to provide indicators when VBI data is active and should be stored. By indicating the VBI data had the same number of lines of the buffer associated with the Graphics Memory 330, the transport stream data can be stored.

Another variation would allow the transport stream data to be stored in the buffer in a compressed form only when it is of a desired type of data. For example, the headers of the transport stream can be monitored to allow only a specific type of data, such as video or audio, to be buffered.

It should be further understood that specific functions and steps described may actually be implemented in hardware and/or in software. For example, the signal generation performed by the Window Controller 520 can be performed by a hardware engine, firmware, such as in microcode, executed on the processing engine, or it may even be performed fully in software. In general, such functions can be performed in a processing module that may be a single processing device or a plurality of processing devices. Such a processing device may be a microprocessor, microcontroller, digital signal processor, microcomputer, portion of the central processing unit, state machine, logic circuitry, and/or any device that manipulates signals (e.g., analog or digital) based on operational instructions. The memory may be a single memory device or a plurality of memory devices. Such a memory device may be a read-only memory, random access memory, floppy disk memory, magnetic tape memory, erasable memory, portion of system memory, and/or any device that stores operational instructions in a digital format. Note that when the processing module implements one or more of its functions via a state machine or logic circuitry, the memory storing the corresponding operational instructions is embedded within the circuitry comprising the state machine and/or logic circuitry.

The specific embodiments of the present invention disclosed herein are advantageous in that transport stream data can be buffered in a portion of memory normally used by the Video

graphics adapter. Furthermore, the amount of overhead needed to store the transport stream data is minimal because the system hardware/firmware/software is largely in place to handle the data. This is an advantage over other prior art embodiments which had dedicated hardware to merely convert transport stream data into a useful format before be received by a VGA controller. By
5 allowing transport stream data to be received directly by the VGA controller, cost savings and efficiencies are gained. In addition, variations of the specific embodiments are anticipated. For example, the uncompressed video may be digitized NTSC/PAL/SECAM signals as well.

CLAIMS

I Claim:

- 1 1. A video graphics system comprising:
2 a transport stream port to receive a digital video transport stream, the digital video
3 transport stream including a data stream and control signals;
4 a transport stream interface control having an input coupled to the transport
5 stream port, and having a first output port to provide a set of control
6 signals, and a second output port to provide video graphics data; and
7 a data storage controller having a first port coupled to the first output port of the
8 transport stream interface control, an address port to provide an address
9 value, and a control port to provide control signals
- 1 2. The system of claim 1 further comprising:
2 a memory having a first port coupled to the second output port of the transport
3 stream interface, a second port coupled to the address port of the data
4 storage controller, and a third port coupled to the control port of the data
5 storage controller.
- 1 3. The system of claim 2, wherein the memory is a frame buffer memory.
- 1 4. The system of claim 2 further comprising:
2 a system bus interface having a first port coupled to a fourth port of the memory,
3 a second port coupled to a fifth port of the memory.
- 1 5. The system of claim 4, wherein the fourth port of the memory is substantially the
2 same as the second port of the memory.
- 1 6. The system of claim 1, wherein the digital video transport stream is a digital video
2 broadcast transport stream.
- 1 7. The system of claim 6, wherein the control signals of the digital video transport stream
2 include a clock signal, a synchronization signal, and a data valid signal. It can also
3 contain an error signal, indicating there is an error in the transport stream.
- 1 8. The system of claim 7, wherein the set of control signals of the transport stream
2 interface control includes a start of field signal to indicate the start of a frame of
3 video.
- 1 9. The system of claim 8, wherein the set of control signals of the transport stream
2 interface control signal includes a valid data output to indicate when data on the
3 second output port of the transport stream interface control is active video data.

1 10. The system of claim 9, wherein the set of control signals of the transport stream
2 interface control includes a valid vertical blanking interval signal to indicate when
3 data on the second output port of the transport stream interface control is present
4 during a vertical blanking interval.

1 11. The system of claim 6, further comprising:
2 a first video port to receive digital video of a first type, wherein the first type is
3 not digital video broadcast stream video;
4 a first video interface control having an input coupled to the first video port, and
5 having a first output port to provide the set of control signals, and a second
6 output port to provide video graphics data; and
7 a select node coupled to the transport stream interface control and to the first
8 video interface control.

1 12. The system of claim 11, wherein the first video port is a zoom video port.

1 13. The system of claim 11, wherein the first video port is a digital video stream port

1 14. A method of receiving video graphics data, the method comprising the steps of:
2 receiving a transport stream associated with a digital video broadcast signal, the
3 transport stream having data signals and control signals;
4 generating a secondary set of controls signals from the transport stream's control
5 signals;
6 storing at least a portion of the transport stream data signals in a memory buffer
7 controlled by the secondary set of control signals; and
8 sending the contents of the memory buffer to a system bus.

1 15. The method of claim 14, further comprising:
2 wherein the steps of receiving generating and storing occur when in a first mode
3 of operation;
4 during a second mode of operation, performing the steps of:
5 receiving a digital video signal having a data signals and a control signals,
6 wherein the digital video signal is of a different type than the
7 transport stream;
8 generating the secondary set of controls signals from the digital video
9 signal's control signals; and
10 storing at least a portion of the digital video signal in the memory buffer
11 based on the secondary set of control signals.
12

13 16. The method of claim 15, wherein the video signal is a Zoom Video signal.

1 17. The method of claim 14, wherein the memory buffer is a frame buffer..

1 18. A system for receiving a digital video broadcast signal, the system comprising:
2 a tuner to receive a digital video broadcast signal and to provide an analog output
3 signal;
4 a demodulator coupled to receive the analog output signal from the tuner, and to
5 provide a transport stream;
6 a video graphics adapter coupled to receive the transport stream and having a
7 system interface port, the video graphics adapter further includes a video
8 engine and a video output port.

1 19. The system of claim 18, wherein the video graphics adapter includes:
2 a memory to store at least a portion of the transport stream.

1 20. The system of claim 18, further comprising:
2 a central processor unit coupled to the system interface port of the video graphics
3 adapter; and
4 a transport demultiplexor coupled to demodulator.

1 21. A method of storing video data, the method comprising the steps of:
2 in a first mode of operation storing pixel information in a frame buffer of a video
3 adapter, wherein one line of frame buffer memory is representative of one
4 line of a video image to be displayed;
5 in a second mode of operation, storing compressed transport stream data in the
6 frame buffer, wherein one line of frame buffer memory is representative of
7 one transport stream packet.

Abstract of the Disclosure

A method and apparatus for storing a compressed video stream or an uncompressed video stream is disclosed. The uncompressed video stream may be Zoom Video data. The compressed video stream may be a transport stream data from a High Definition television (HDTV) broadcast. A Video Graphics Adapter is configured to properly receive one of the two types of video data. The received data and control signals are monitored to provide a second set of control of data signal which are used by a packer and an window control to provide data of a predetermined width and control to an address generator. The data is buffered within a graphics memory such as a frame buffer. The graphics memory can be written to system memory when full, or accessed by the system memory controller during the fill operation if a multi-ported memory is used.

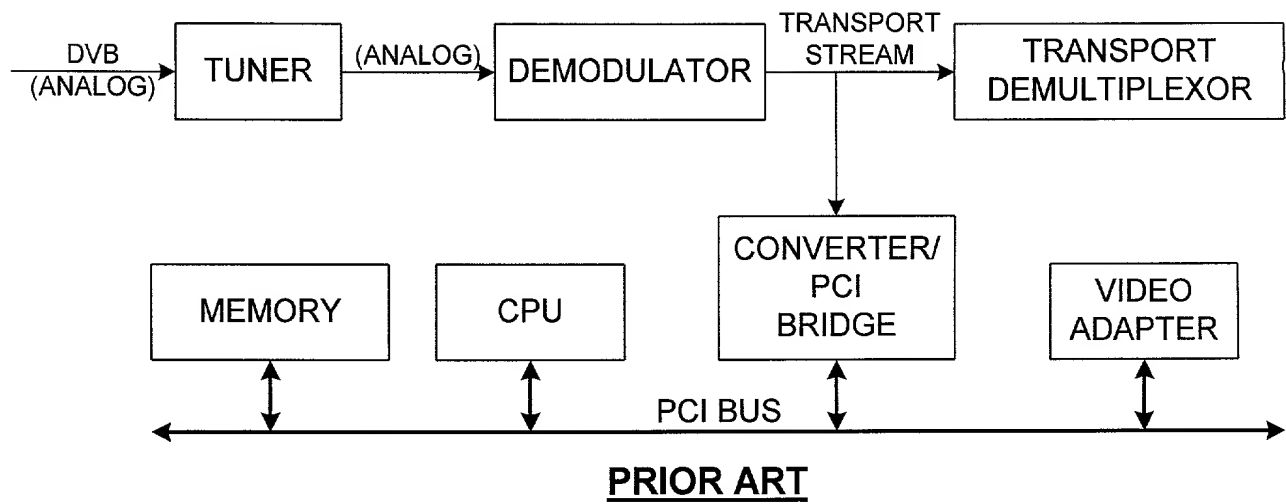


FIG. 1

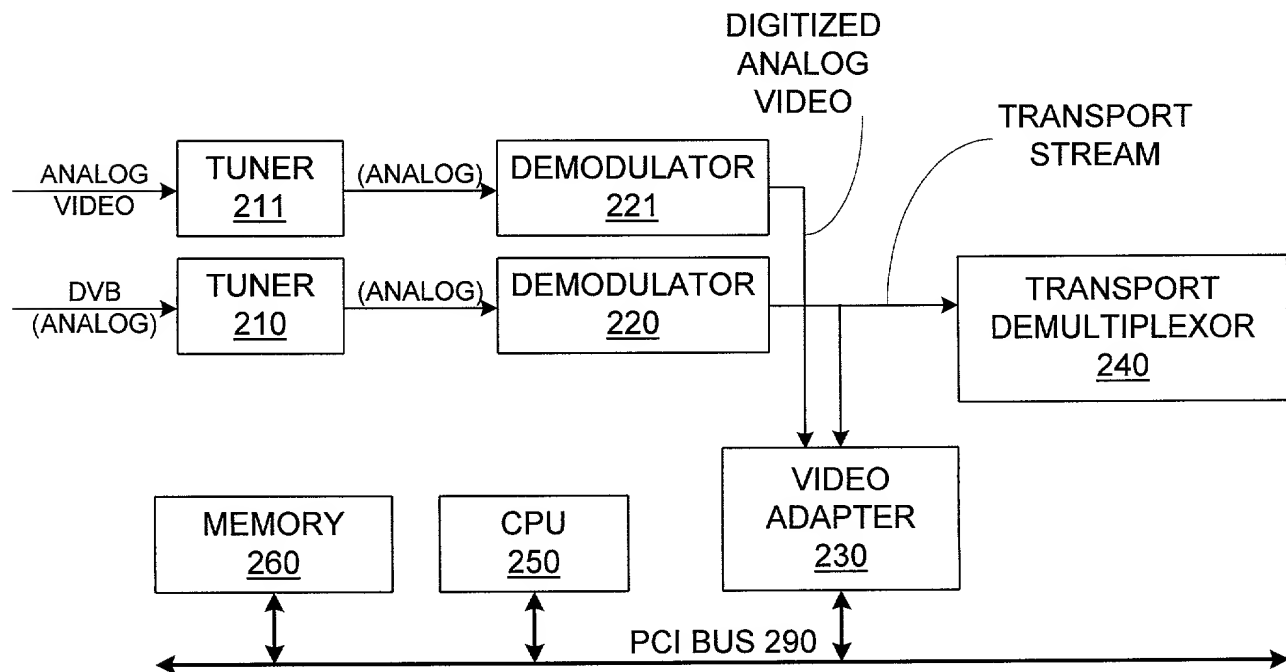


FIG. 2

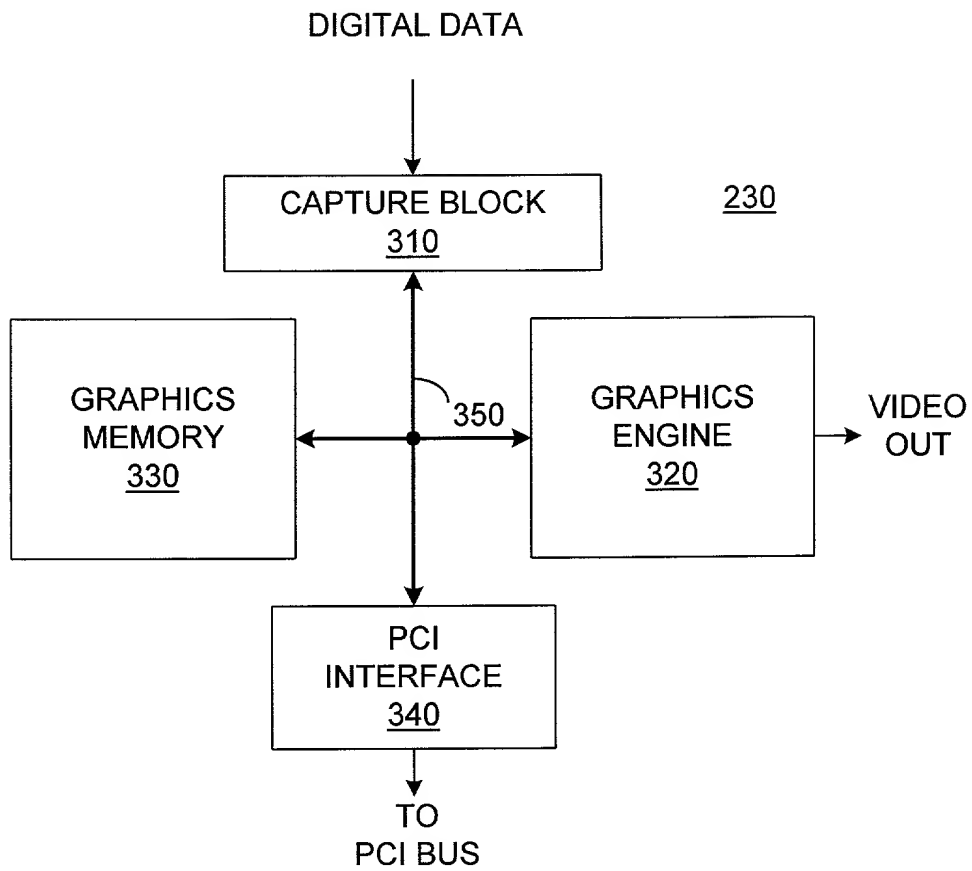


FIG. 3

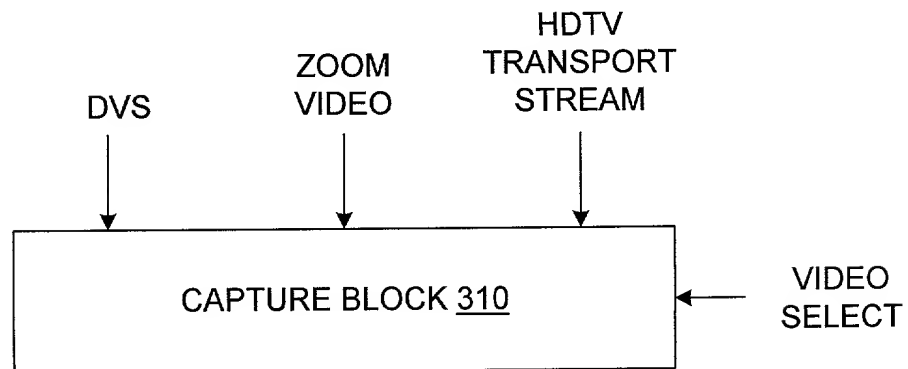


FIG. 4

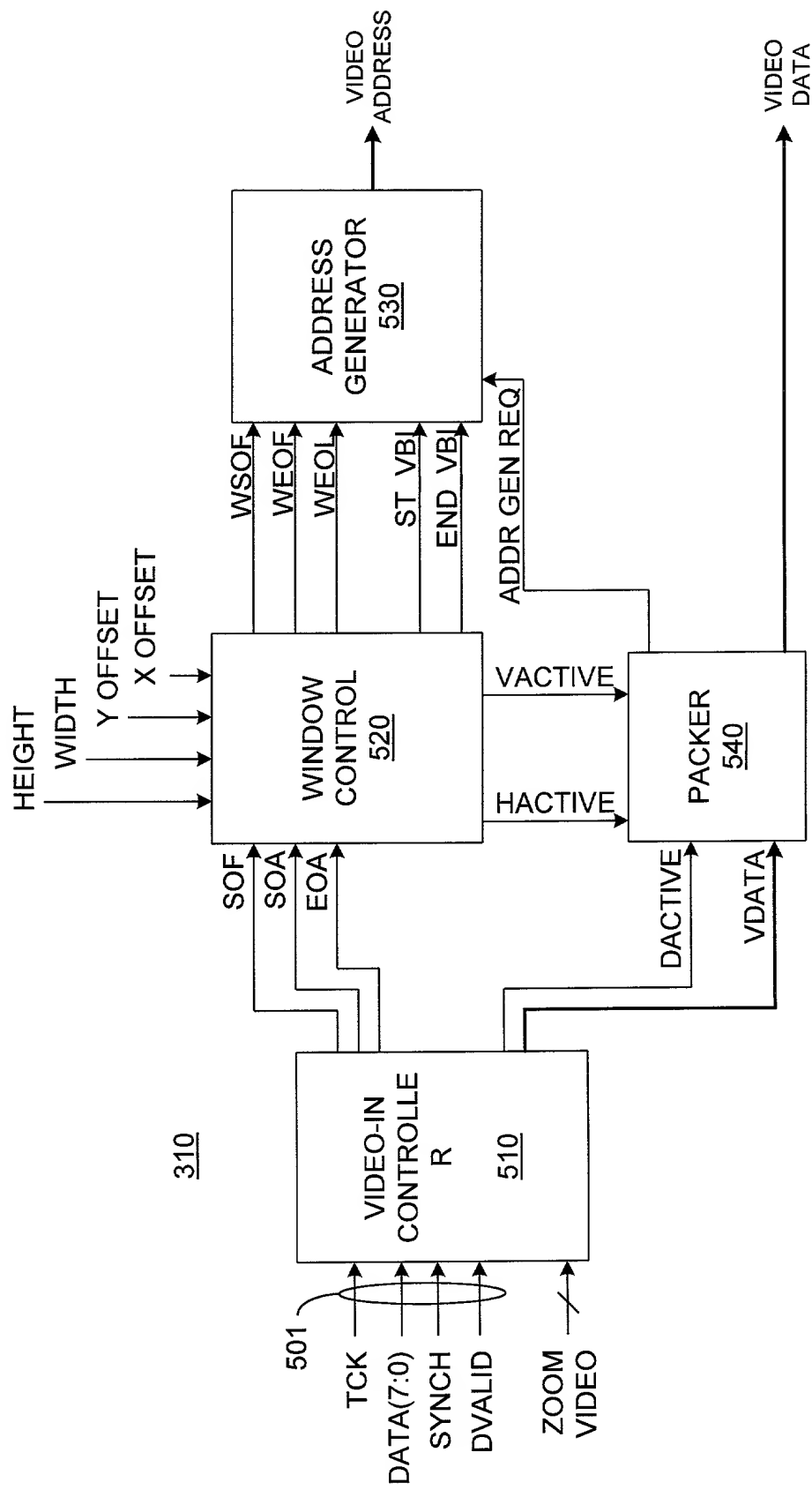


FIG. 5

644F00-2F6B9E00

SOA

EOA

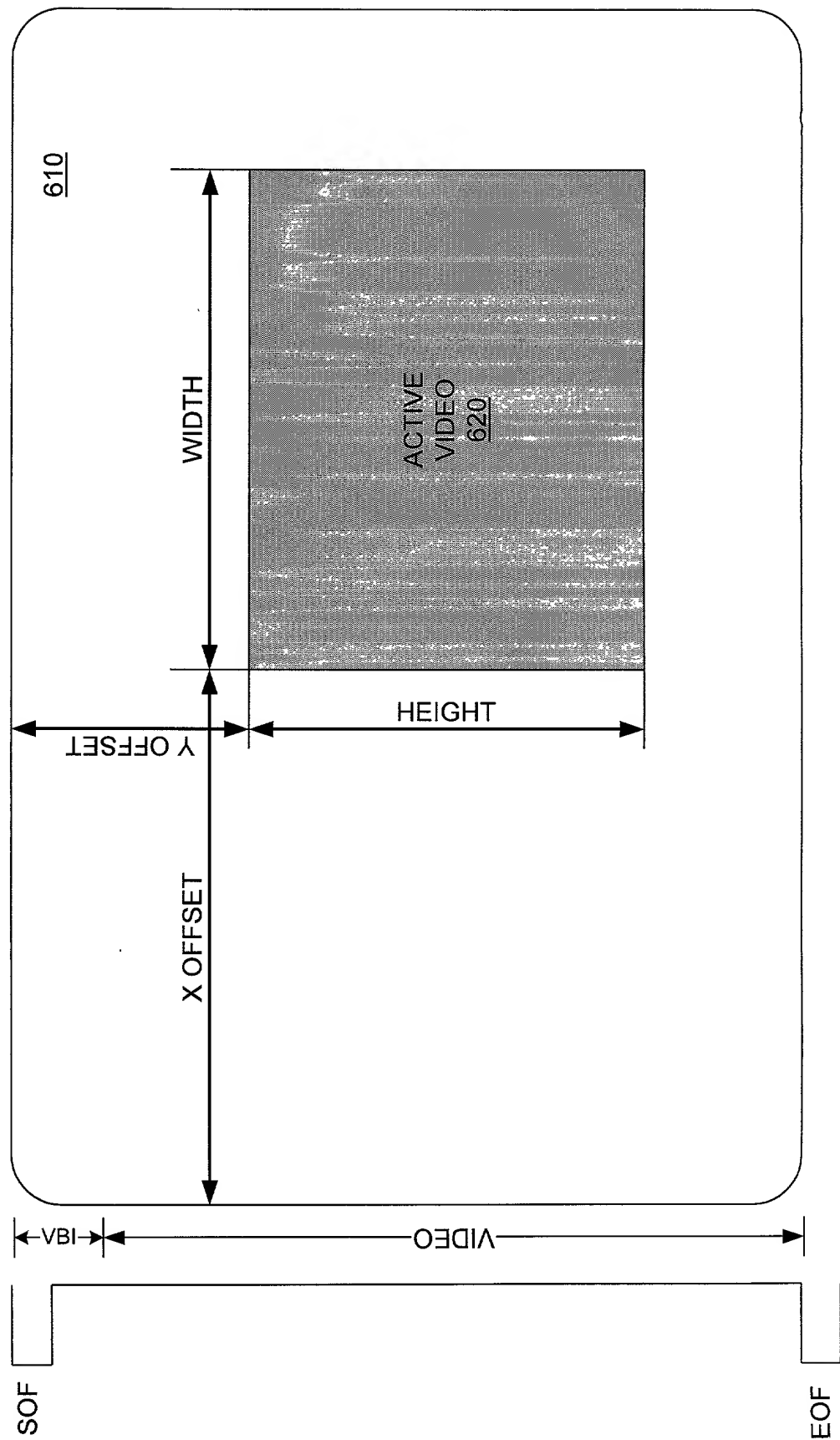


FIG. 6

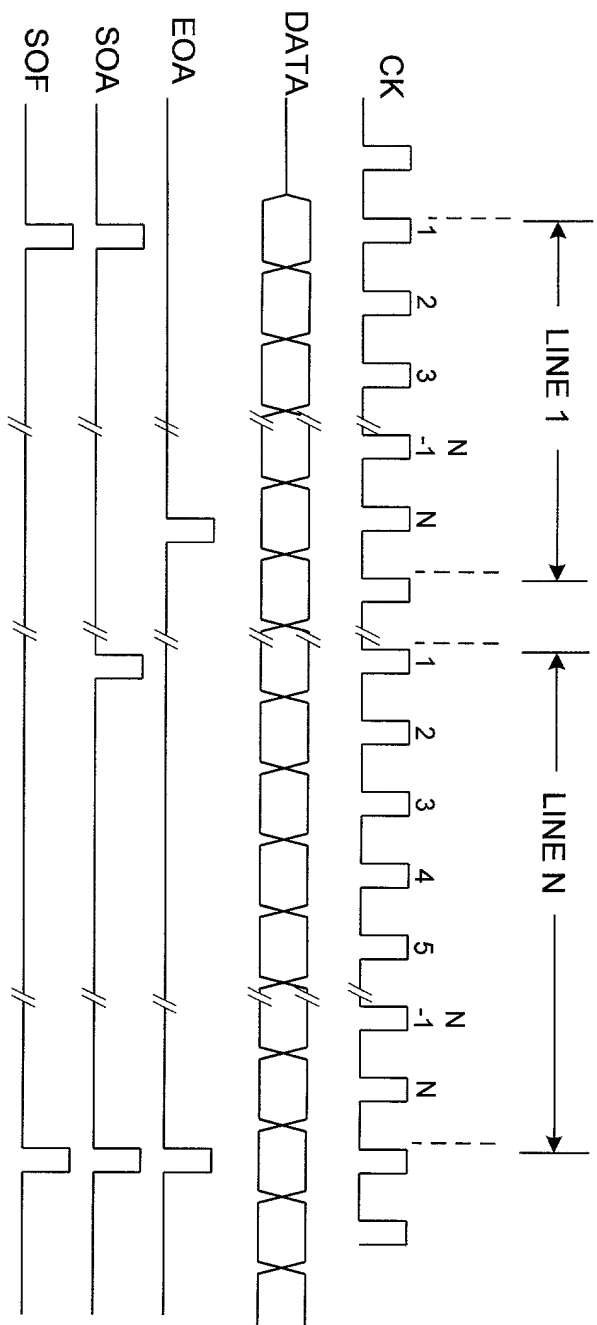


FIG. 7

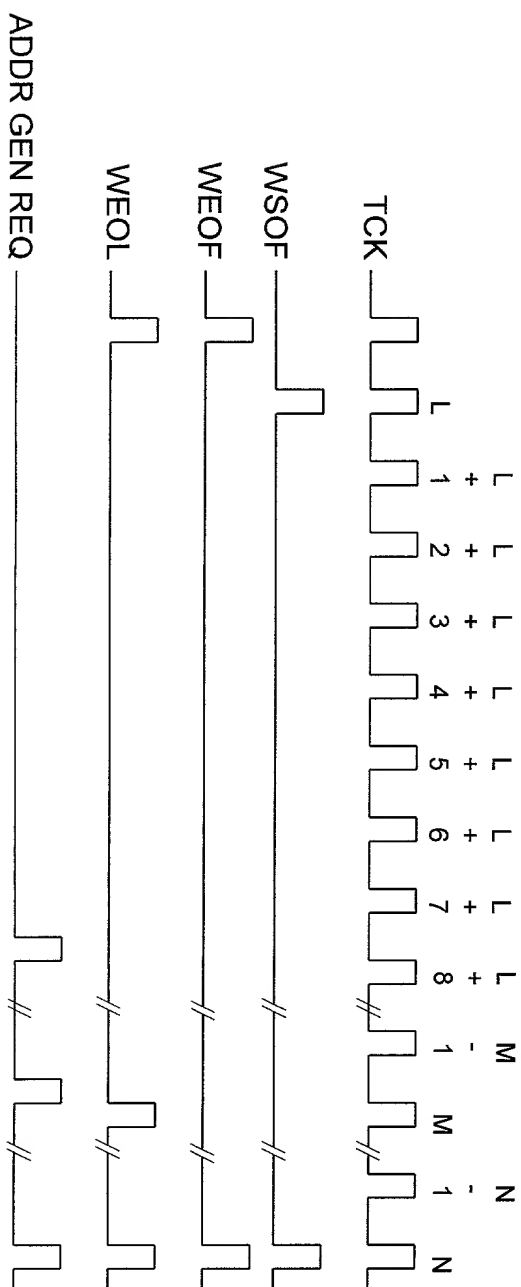


FIG. 8

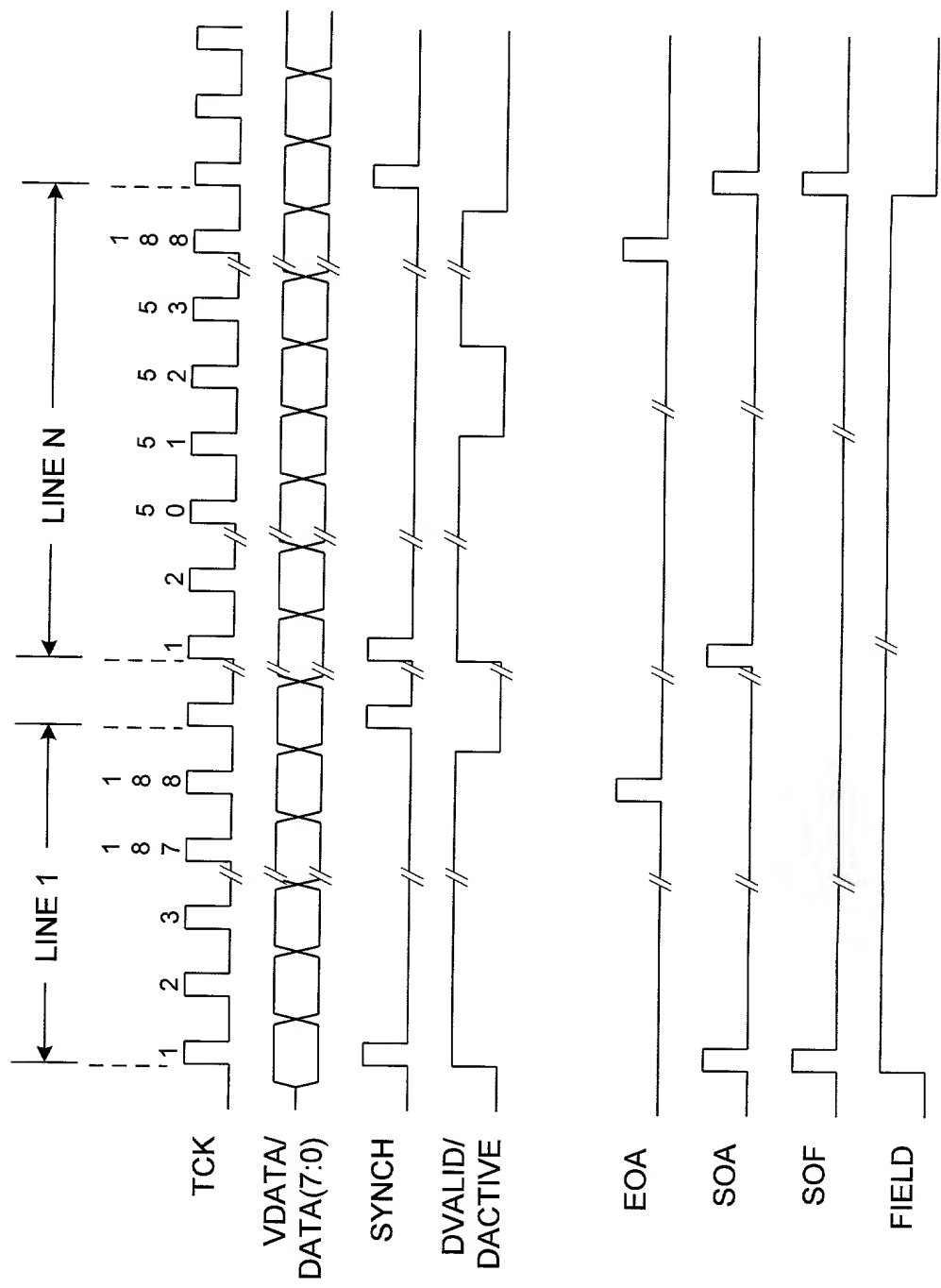


FIG. 9

ADDR	DATA(0:N)	V
00	LINE 0	
01	LINE 1	
02	LINE 2	
03	LINE 3	
04	LINE 4	
.		
.		
.		
FE	LINE 254	
FF	LINE 255	

FIG. 10

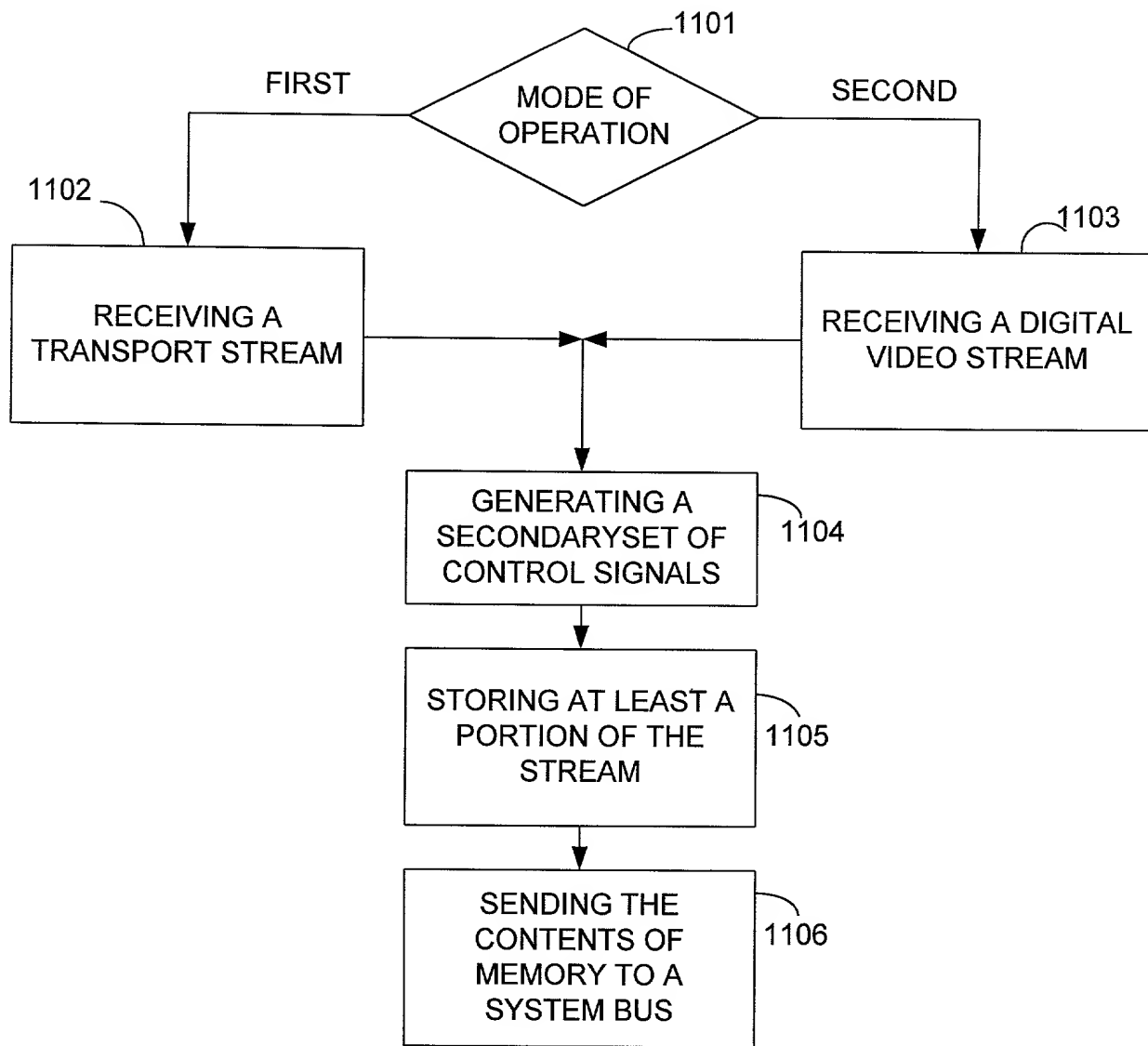


FIG. 11

**DECLARATION
FOR UTILITY OR DESIGN
PATENT APPLICATION
(37 CFR 1.63)**

- ☒ Declaration Submitted with Initial Filing, OR
☐ Declaration Submitted after Initial Filing
(surcharge (37 CFR 1.16 (e)) required)

Attorney Docket Number 0100.9900680
First Named Inventor Klebanov, et al
COMPLETE IF KNOWN
Application Number
Filing Date
Group Art Unit
Examiner Name

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **METHOD AND APPARATUS FOR RECEIVING DIGITAL VIDEO SIGNALS**

the specification of which:

- ☒ is attached hereto.
☐ was file on (MM/DD/YYYY) as United States Application Number or PCT International Application Number and was amended on (MM/DD/YYYY) (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	Certified Copy Attached?	
				YES	NO
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

- ☐ Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

I hereby claim the benefit under 35 U.S.C. 119(c) of any United States provisional application(s) listed below.

Application Number(s)	Filing Date (MM/DD/YYYY)

- ☐ Additional provisional application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application or PCT Parent Number	Parent Filing Date (MM/DD/YYYY)	Parent Patent Number (if applicable)

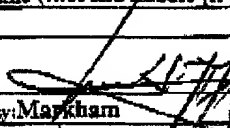
- ☐ Additional U.S. or PCT international application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

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DECLARATION	ADDITIONAL INVENTOR(S) Supplemental Sheet	Page 1 of 1 Attorney Docket Number 0100.9900680
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Name of Additional Joint Inventor:

☐ A petition has been filed for this unsigned inventor

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Country:			

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City:		State:	ZIP:
Country:			

Name of Additional Joint Inventor:

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Inventor's Signature		Date	
Residence	City:	State:	Country:
Post Office Address			
City:		State:	ZIP:
Country:			

2024 FEB 27 10:00 AM

Figure 11 illustrates, in flow diagram form, a method in accordance with the present invention.

Detailed Description of the Drawings

5 A method and apparatus for receiving one of a compressed video stream and an uncompressed video stream is disclosed. The uncompressed video stream may be Zoom Video data or a digitized analog video signal. The compressed video stream may be an MPEG transport stream data from a High Definition television (HDTV) broadcast. A Video Graphics Adapter is configured to properly receive one of the two types of video data. The received data
10 and control signals are monitored to provide a second set of control signals that are used by a packer, a window control, and an address generator. The packer packs data into a format which is compatible with the frame buffer memory. The window controller controls the amount of data written into frame buffer memory. The address generator generates proper frame buffer addresses for the data. The data is stored within a pre-defined area of graphics memory such as a
15 frame buffer. The data can be transferred to system memory when a buffer is full.

Figure 2 illustrates a system in accordance with the present invention. A digital video broadcast (DVB) signal is received by the tuner 210. The tuner 210 provides a representation of the received analog signal to the demodulator 220. The demodulator 220 demodulates the signal to provide a digital TRANSPORT STREAM to one or both of the Transport Demultiplexor 240,
20 and the Video Adapter 230. In accordance with the present invention, the Video Adapter 230 receives the TRANSPORT STREAM and buffers it into a video memory, or frame buffer. Upon filling the frame buffer, the transport stream data is either further utilized by the Video Adapter 230, or at least partially provided to system components, such as the central processing unit (CPU) 250, or the memory 260. A second data path receives an analog signal, such as would
25 normally be associated with television broadcasts, at tuner 211. The signal from tuner 211 is provided to a NTSC/PAL/SECAM demodulator 221 to provide a digital representation of the received analog signal. Not that the tuner 211 and demodulator 221 may be the same, or different, from the tuner 210 and demodulator 211.

Figure 3 illustrates the Video Adapter 230 in greater detail. In normal operation, the Video Adapter 230 processes video and/or graphics information and provides a signal labeled VIDEO OUT. The VIDEO OUT signal is used to drive an image onto an external display device (not shown). In accordance with the present invention, the Video Adapter 230 includes a

5 Capture Block 310 to receive a data stream, a graphics engine 320, a graphics memory 231, and a PCI interface 340.

In operation, the a digital data stream, such as the TRANSPORT STREAM from the demodulator 220 of Figure 2, is received at the capture block 310. Where the digital data stream is a TRANSPORT STREAM, the TRANSPORT STREAM comprises both data, and control

10 information. Generally, the TRANSPORT STREAM includes a plurality of packets. Each packet of transport stream information includes a synchronization byte followed by a predetermined number of data bytes. The data bytes can include routing information stored as header information, or as raw data as identified by the header information. When the data is transmitted as header information, it is in an uncompressed form that indicates the type of data

15 that is to follow the header. A single header can be included in one or more packets.

The Capture Block 310 receives the TRANSPORT STREAM information and provides the necessary data and control in order to store the compressed transport stream data within the Graphics Memory 330. The memory 330 is accessed over the bus 350. In a specific embodiment, the memory 330 is a frame buffer used by the Graphics Engine 320. The Memory

20 330 is connected to the Capture Block 310 through the bus 350 which accommodates the necessary data, address, and control lines for accessing memory 330. The graphics memory 330 is also connected to the graphics engine 320 and the PCI Interface 340 through the bus 350.

In accordance with the present invention, the graphics Memory 330 can operate as a frame buffer for the Graphics Engine 320, or as a buffer to store the TRANSPORT STREAM

25 data. The PCI Interface 340 provides an interface between the internal bus 350 to an external PCI bus. Generally, the PCI Bus will be a system bus. It should be noted that while a PCI interface 340 has been shown, the present invention anticipates the use of other type bus interfaces. Therefore, the PCI interface 340 may be any bus type whether an industry standard or a proprietary interface.